High Resolution Visible Imager

Date Revised: 17 JAN 03

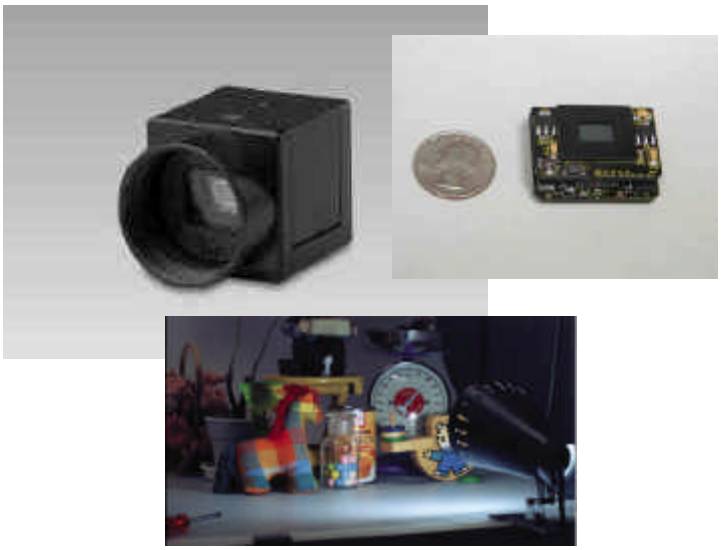
VENDOR DESCRIPTION

RSC has developed a compact, low power, high resolution camera for applications on unmanned platforms. The camera is based on a commercially developed CMOS imager.

The CMOS imaging chips include SOC functional integration and consume 180 mW. Comparable CCD imagers with separate clock and A/D chips consume 2.5 W.

Chip format is HDTV-compatible (2K x 1K) and can be provided as a panchromatic camera or with integrated RGB color filters. Integrated micro-lenses enable high quantum efficiency. Multiple digital or analog interfaces are available.

Selected area readout plus compression enable operation in limited communication bandwidth systems.



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EOIR

Electro-Optical (EO)
Type: CMOS
Resolution: 1936 x 1090, 5 micron pixels
Noise electrons: 25
12 bit integral A/D
Angular Coverage: Can accommodate various commercial lenses
Modes of Operation: 30 Hz Progressive scan
Selective windowing readout
Rolling shutter
Field of View: Can accommodate various commercial lenses
Sensitivity: 0.2-1.0 microns (unfiltered)
Color or B/W: Both

Hardware
Power: 500 mW (Camera), 180 mW (CMOS chip)
Weight: 220 grams (including 3mm lens)
Dimensions: 37mm x 31mm x 25mm